

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In application of: **Alon et al** Date: October 9, 2006  
Serial No.: 10/091,934 Group Art Unit: 2825  
Filed: March 06, 2002 Examiner: Naum B Levin  
Title: AN INTERCONNECT-AWARE METHODOLOGY FOR INTEGRATED  
CIRCUIT DESIGN

Honorable Commissioner for Patents  
P.O. 1450  
Alexandria, Virginia 22313-1450

**Amendment**

Sir:

This amendment is filed in response to the Final Office Action dated August 8, 2006 issued by the USPTO in connection with the above-identified patent application. A response to the August 8, 2006 Office Action is due November 8, 2006. The two-month deadline was due on Sunday, October 8, 2006. Accordingly, this Amendment is being timely filed within the two-month deadline.

Kindly amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 3 of this paper.